FAN, Hanwei

Educational Background

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The Hong Kong University of Science and Technology	09/2023-Now
PhD, major in Electronic and Computer Engineering	
Supervisor: Prof. Wei ZHANG	
Research Topic: Computer Architecture, Design Automation	
The Hong Kong University of Science and Technology	09/2021-08/2023
MPhil, major in Microelectronics	
Supervisor: Prof. Wei ZHANG	
Thesis Title: Bayesian Optimization with Clustering and Rollback for CNN Auto Pruning	
The Hong Kong University of Science and Technology	00/2010 06/2020
The Hong Kong University of Science and Technology	09/2019-06/2020
MSc, major in Electronic Engineering	
Beijing Institute of Technology	09/2015-06/2019
BEng, major in Automation	07/2013-00/2017
Graduation Project: Three Phase T-NPC Electrical Load Simulator based on FPGA-DSP Microcontroller	
Work Experiences	
Research Intern - Computing Technology Lab, Alibaba DAMO Academy	06/2022-12/2022
• Explainable machine learning for micro-architecture design space exploration	
Cost-aware neural architecture search	
Research Assistant - Reconfigurable Computing Systems Lab, HKUST	09/2020-08/2021
Research Assistant - Reconfigurable Computing Systems Lab, HKUST Large-scale system verification framework 	09/2020-08/2021
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Large-scale system verification frameworkA Bayesian optimization framework for CNN auto compression	09/2020-08/2021
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